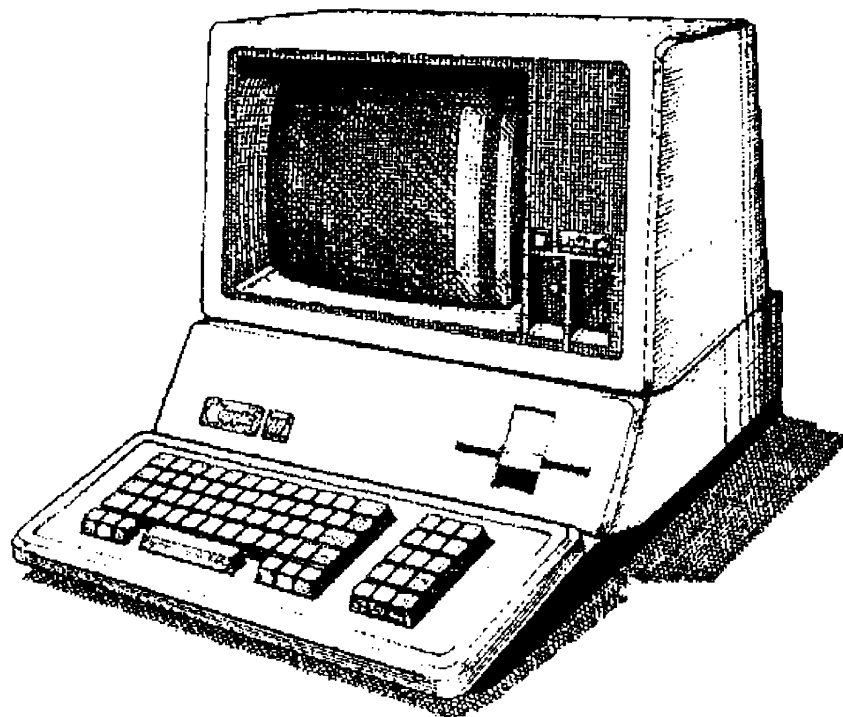




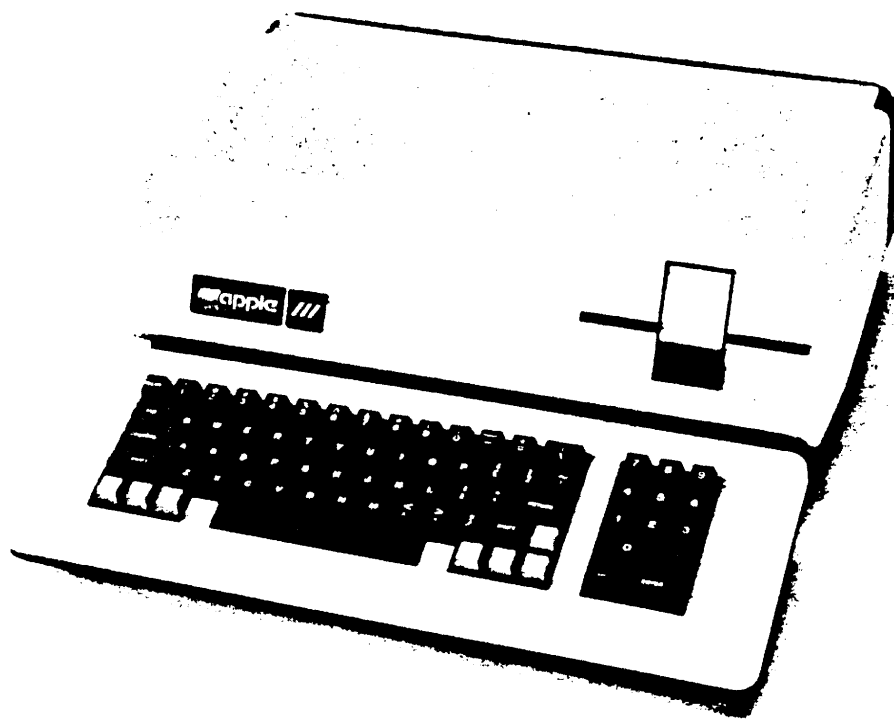
Apple /// Computer Information

Apple /// Service Reference Manual



Theory of Operation • Servicing Information

Written by Apple Computer • 1982



APPLE III SERVICE REFERENCE MANUAL



To the Reader,

This manual was developed for all the A/// Service Technicians at our Level II Regional Service Center. The intent of this book is to help you understand and repair the Apple ///. The book is partitioned into two sections: Theory of Operation and Servicing Information. There is sufficient information in this manual so that an inexperienced technician can be productive in a short time. This manual should help you understand and appreciate the Apple ///.

In Appreciation:

Although many people have helped me with this manual I wish to particularly thank the following people for their contribution to this manual:

Bill Holman
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Ed Goodwin
Sandy Sanford
Peter Quinn

Thanks!

Bob Cummings
Bob Cummings

APPLE III REFERENCE MANUAL

If you are interested in how the Apple /// works, or if you want to trouble shoot your machine, you should have this manual. As you can see from the table of contents (shown below), this comprehensive manual covers virtually every facet of the Apple ///. Like new condition, binder, 17 chapters, 460 pages (8-1/2 x 11), two diagnostic diskettes.....\$150.

TABLE OF CONTENTS

VIDEO DISPLAY LOGIC

- Display Modes
- Introduction
- 40 Character Apple II
- 40 Character Apple III
- 80 Character Black & White Apple III
- 80 Character Black & White Hires
- Medium Resolution 16 Color Graphics Apple III
- Super Hires Apple III
- Apple III Hires
- Super Hires Apple III
- Video Appendix

THEORY OF OPERATION

- INTRODUCTION
- General Description
- Simplified Functional Description
- The Main Logic Board
- The Memory Board
- The Keyboard PCB
- The Disk Drive
- The Apple /// Power Supply

- MEMORY & MEMORY ADDRESSING
- Introduction to the Apple III Memory
- Simplified Memory Logic
- Memory Addressing: Block Flow
- The Processor
- Memory Address Multiplexer
- RAS/CAS Decode
- Alternate Stack
- Memory & Memory Addressing Appendices

THE VERSATILE INTERFACE ADAPTER

- General
- VIA 6522 Pin Descriptions
- VIA Functional Description
- Interrupt Operation
- 6522 VIA Environment and Control
- VIA (FFDX)
- VIA (FFEX)
- VIA Appendices

THE 6551 ASYNCHRONOUS COMMUNICATIONS INTERFACE ADAPTER

- General
- Status Register
- Command Register
- Control Register
- Simple Serial Port

SYSTEM CLOCKS & TIMING

- Main Clock CL44
- Frequency Divider
- "Q" Timing
- HPE's Freeze
- AR, PREIM, & CHM
- RAS (Row Address Strobe[Select])
- Video Horizontal & Vertical State Counters
- Horizontal Section
- Vertical Section

THE SYSTEM MONITOR

- Built-in I/O Locations
- Peripheral I/O
- The Joystick Ports
- Peripheral Connector Pinout
- Peripheral Connector Signal Description
- ROM Listings

SCHEMATIC DIAGRAMS

- Annotated Schematic Diagrams
- DISK SUBSYSTEM
- Theory of Operation
- Disk Conditioning Circuit
- Analog Card

SERVICING INFORMATION

- TESTING & TROUBLESHOOTING
- Apple III Final Test Procedure
- 16 Sector Disk III Final Test (1000T)
- 5 Volt Memory Board Ram Troubleshooting Procedure
- The Apple III Troubleshooting Flowchart (Component)

PARTS LAYOUT AND PARTS LIST

- Apple III I/O Parts by Location
- Apple III Indented Bill of Materials

WIRE LIST

- Wire List
- MODULE REPLACEMENT PROCEDURES & ASSEMBLY DRAWINGS
- Apple III Module Replacement Procedures
- Apple III Dealer Service Diagnostics Reference
- Apple III Assembly Drawings

GENERAL APPENDIX

- Apple III System Overview
- Apple III System Monitor
- Apple III Logic Signal Source
- Main Logic Board Circuit Function Areas
- How to Read Prom (Rom) Logic Expressions
- Prom (ROM) Logic Expressions
- Ascii Conversion Tables

LIST OF TABLES & ILLUSTRATIONS

- AIII System Block Diagram (Illustration)
- AIII System Block Diagram - Detailed (Illustration)
- System Functional Block Diagram
- The 12 Volt Memory Board (128K Configuration)
- The 5 Volt Memory Board (128K Configuration)
- The Apple III Main Logic Board (Module)
- The Apple III Detailed System Functional Block Diagram
- The Apple III Memory Map
- Simplified Memory Address Block Diagram
- Block Diagram Memory Address Logic
- RAS/CAS Decode Logic Block Diagram
- Indirect Addressing (Listing)
- Memory & Memory Addressing Appendix
- Apple III Memory Map
- Memory Map Space Allocations
- Address Logic Truth Table
- The Apple III Memory Board (12V)
- The Apple III Memory Board (5V)
- The 5 Volt Memory Board (256K)
- How to READ FROM (ROM) Logic Expressions
- RAM Registers
- MPU Registers
- Block Diagram of the 6522 VIA
- Addressing 6522 VIA Internal Register
- VIA FFDX Internal Register Summary
- VIA FFEY Internal Register Summary
- VIA Appendices
- The ACIA Block Diagram
- ACIA Control Register
- ACIA Command Register
- ACIA Status Register
- ACIA Pin Configuration
- Main Clock 14Mhz Circuit
- Frequency Divider Circuit
- A/// System Timing Diagrams
- 1 to 2 Whz Gearshift
- Video State Counter
- Video Scan Decode ROM
- Video Appendix
- Apple III Video Logic Block Diagram
- Hires Mode Page 1, 8/M, 280 X 192
- Hires Mode Page 2, 8/M, 280 X 192
- 280 X 192 Color Hires Mode Page 1
- 280 X 192 Color Hires Mode Page 2
- Super Hires Mode Page 1
- Super Hires Mode Page 2
- Ahires Test Page 1

AND MORE!



TABLE OF CONTENTS

<u>SECTION I.</u>	<u>THEORY OF OPERATION</u>	<u>PAGE</u>
CHAPTER 1	<u>INTRODUCTION</u>	
	General Description	1.1
	Simplified Functional Description	1.4
	The Main Logic Board	1.4
	The Memory Board	1.6
	The Keyboard PCB	1.7
	The Disk Drive	1.7
	The Apple /// Power Supply	1.7
CHAPTER 2	<u>MEMORY & MEMORY ADDRESSING</u>	
	Introduction to the Apple /// Memory	2.1
	Simplified Memory Logic	2.1
	Memory Addressing: Block Flow	2.4
	The Processor	2.4
	Memory Address Multiplexer	2.6
	RAS/CAS Decode	2.6
	Alternate Stack	2.10
	Memory & Memory Addressing Appendices	2.12
CHAPTER 3	<u>THE VERSATILE INTERFACE ADAPTER</u>	
	General	3.1
	VIA 6522 Pin Descriptions	3.1
	VIA Functional Description	3.6
	Interrupt Operation	3.8
	6522 VIA Environment and Control	3.10
	VIA (FFDX)	3.12
	VIA (FFEX)	3.15
	VIA Appendices	3.18
CHAPTER 4	<u>THE ACIA</u>	
	The 6551 Asynchronous Communications Interface Adapter	4.1
	Status Register	4.3
	Command Register	4.4
	Control Register	4.5
	Simple Serial Port	4.6
CHAPTER 5	<u>SYSTEM CLOCKS & TIMING</u>	
	Main Clock Cl4M	5.1
	Frequency Divider	5.1
	"Q" Timing	5.1
	HPE* Freeze	5.5
	Ax, PRE1M, & ClM	5.5
	RAS (Row Address Strobe[Select])	5.5
	Video Horizontal & Vertical State Counters	5.7
	Horizontal Section	5.7
	Vertical Section	5.10



CHAPTER 6	<u>VIDEO DISPLAY LOGIC</u>	
	Display Modes	6.1
	Introduction	6.2
	40 Character Apple][6.2
	40 Character Apple ///	6.3
	80 Character Black & White Apple ///	6.3
	Black & White Hires	6.4
	Medium Resolution 16 Color Graphics Apple ///	6.4
	Super Hires Apple ///	6.5
	Apple /// Hires	6.5
	Super Hires Apple ///	6.7
	Video Appendix	6.9
CHAPTER 7	<u>INPUT/OUTPUT</u>	
	Description	7.1
	Interface Control Signals	7.1
	Interrupts	7.2
	Addressing the I/O	7.2
	The Input Operation	7.3
	The Output Operation	7.3
	System Timing	7.3
	the A/// Joystick	7.5
CHAPTER 8	<u>THE KEYBOARD</u>	
	The Keyboard	8.1
	Reading the Keyboard	8.1
	Keyboard Codes	8.5
	The Apple][Emulation Mode	8.7
	Electronic Circuit Description	8.7
	The Repeat Function	8.8
	The Reset Function	8.8
	Keyboard Light	8.8
CHAPTER 9	<u>POWER SUPPLY</u>	
	The Apple /// Power Supply	9.1
	The Basic Switching Power Supply	9.1
	How it works!	9.3
	Detailed Hardware Description	9.6
	Schematic	9.9
	Parts List	9.10
	Component Layout	9.13
CHAPTER 10	<u>A][EMULATION</u>	
	A][Emulation Restrictions	10.1
	The Color Video Connector	10.2
	The High-Resolution Graphics (Hi-Res) Mode	10.3
	The Speaker	10.3
	The Cassette Interface	10.3
	Input/Output Special Locations	10.4
	A/D Selection	10.4
	Analog Inputs	10.5
	Strobe Output	10.5
	Autostart ROM/Monitor ROM	10.5



	The System Monitor	10.6
	Built-In I/O Locations	10.8
	Peripheral I/O	10.10
	The Joystick Ports	10.11
	Peripheral Connector Pinout	10.14
	Peripheral Connector Signal Description	10.15
	ROM Listings	10.18
CHAPTER 11	<u>SCHEMATIC DIAGRAMS</u>	
	Annotated Schematic Diagrams	11.1
CHAPTER 12	<u>DISK SUBSYSTEM</u>	
	Theory Of Operation	12.1
	Disk Conditioning Circuit	12.1
	Analog Card	12.4
<u>SECTION II.</u>	<u>SERVICING INFORMATION</u>	
CHAPTER 13	<u>TESTING & TROUBLESHOOTING</u>	
	Apple /// Final Test Procedure	13.1
	16 Sector Disk /// Final Test (1000T)	13.9
	Apple /// Troubleshooting (Module Level)	13.11
	5 Volt Memory Board Ram Troubleshooting Procedure	13.16
	The Apple /// Troubleshooting Flowchart (Component)	13.28
CHAPTER 14	<u>PARTS LAYOUT AND PARTS LIST</u>	
	Apple /// IC Parts by Location	14.1
	Apple /// Indented Bill of Materials	14.4
CHAPTER 15	<u>WIRE LIST</u>	
	Wire List	15.1
CHAPTER 16	<u>MODULE REPLACEMENT PROCEDURES & ASSEMBLY DRAWINGS</u>	
	Apple /// Module Replacement Procedures	16.1
	Apple /// Dealer Service Diagnostics Reference	16.25
	Apple /// Assembly Drawings	16.32
CHAPTER 17	<u>GENERAL APPENDIX</u>	
	Apple /// System Overview	17.1
	Apple /// System Monitor	17.3
	Apple /// Logic Signal Source	17.5
	Main Logic Board Circuit Function Areas	17.9
	How to Read Prom (Rom) Logic Expressions	17.10
	Prom (ROM) Logic Expressions	17.11
	Ascii Conversion Tables	17.19



LIST OF TABLES & ILLUSTRATIONS

AIII System Block Diagram (Illustration)	1.2
AIII System Block Diagram - Detailed (Illustration)	1.3
System Functional Block Diagram	1.5
The 12 Volt Memory Board (128K Configuration)	1.6
The 5 Volt Memory Board (128K Configuration)	1.7
The Apple /// Main Logic Board (Module)	1.8
The Apple /// Detailed System Functional Block Diagram	1.9
The Apple /// Memory Map	2.2
Simplified Memory Address Block Diagram	2.3
Block Diagram Memory Address Logic	2.5
RAS/CAS Decode Logic Block Diagram	2.7
Indirect Addressing (Listing)	2.8
Memory & Memory Addressing Appendix	2.12
Apple /// Memory Map	2.13
Memory Map Space Allocations	2.17
Address Logic Truth Table	2.18
The Apple /// Memory Board (12V)	2.19
The Apple /// Memory Board (5V)	2.20
The 5 Volt Memory Board (256K)	2.21
How to READ PROM (ROM) Logic Expressions	2.22
RAM Logic Expression	2.23-2.28
MPU Registers	2.29
Block Diagram of the 6522 VIA	3.2
Addressing 6522 VIA Internal Register	3.2
VIA FFDX Internal Register Summary	3.11
VIA FFEX Internal Register Summary	3.14
VIA Appendices	3.18
The ACIA Block Diagram	4.2
ACIA Control Register	4.11
ACIA Command Register	4.11
ACIA Status Register	4.12
ACIA Pin Configuration	4.12
Main Clock 14Mhz Circuit	5.2
Frequency Divider Circuit	5.2
A/// System Timing Diagrams	5.3-5.6
1 to 2 Mhz Gearshift	5.8
Video State Counter	5.8
Video Scan Decode ROM	5.9
Video Appendix	6.9
Apple /// Video Logic Block Diagram	6.10
Hires Mode Page 1, B/W, 280 X 192	6.11
Hires Mode Page 2, B/W, 280 X 192	6.12
280 X 192 Color Hires Mode Page 1	6.13
280 X 192 Color Hires Mode Page 2	6.14
Super Hires Mode Page 1	6.15
Super Hires Mode Page 2	6.16
Ahires Test Page 1	6.17



Ahires Test Page 2	6.18
Color Bar & Gray Scale Test	6.19
Apple][Text Mode Page 1	6.20
Apple][Text Mode Page 2	6.21
Sara 40 Column Text Mode Test	6.22
Sara 80 Column Text Mode Test	6.23
Video Mode Truth Tables	6.24
Video Circuit Schematic	6.25
Video Output types	6.26
Video Output Type (Circuit diagrams)	6.27
Video ROM Logic Expressions	6.29
Video ROM Circuit truth table representation	6.30
Color Video Connector Description	6.31
I/O System Timing Diagram	7.4
Peripheral Connector Pinout	7.7
Peripheral Connector Signal Description	7.8
Pin Signal Assignment	8.9
Keys & Associated ASCII Codes	8.10
Keyboard Circuit Schematic	8.12
Reset Circuit Schematic	8.13
A/// Power Supply Parts Layout	9.2
Switching Power Supply Block Diagram	9.4
Switching Power Supply Circuit Block Diagram	9.5
A/// Power Supply Schematic Diagram	9.8
A/// Power Supply Parts List	9.10
A/// Power Supply Parts Layout	9.13
A/// Schematic Diagrams	11.1-11.12
Disk Conditioning Circuit Schematic	12.2
Analog Card A/// Circuit Schematic	12.5
A/// Internal Disk Assembly Drawing	12.8
Install Disk Drive Illustration	12.9
Apple /// Disk Enables	12.10
Main Logic Board IC Designators	14.3
Apple /// Logic Signal Source	17.5
Main Logic Board Circuit Function Areas	17.9
How to Read Prom (Rom) Logic Expressions	17.10
Prom (ROM) Logic Expressions	17.11
Ascii Conversion Tables	17.19